

IN THE CLAIMS:

1.-50. (Cancelled)

51. (Previously Presented) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode, the plasma display panel driving method including the following step to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s, and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

52. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than 5.25 V/ μ s and no greater than 6 V/ μ s.

53. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than 4.2 V/ μ s and no greater than 5.25 V/ μ s.

54. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

55. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

56. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

57. (Previously Presented) The plasma display panel driving method according to Claim 51,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

58. (Previously Presented) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode, the plasma display panel driving method including the following steps to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells; and

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells based on image data input,

10 wherein in the set-up step, the set-up pulse is applied via the first electrodes and has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising, and

wherein in the write step, the write pulse is applied to the selected discharge cells via the first electrodes.

59. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $5.25 \text{ V}/\mu\text{s}$ and no greater than $6 \text{ V}/\mu\text{s}$.

60. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

61. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

62. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the setup step rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

63. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

64. (Previously Presented) The plasma display panel driving method according to Claim 58,

wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

65. (Previously Presented) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of a first electrode and a second electrode, and

a plurality of discharge cells with walls, each discharge cell having one of the
5 pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by including a set-up period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge cells, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse

having a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that
10 starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

66. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $5.25 \text{ V}/\mu\text{s}$ and no greater than $6 \text{ V}/\mu\text{s}$.

67. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

68. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

69. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

70. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

71. (Previously Presented) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

72. (Previously Presented) A plasma display apparatus comprising:
a plasma display panel that includes a plurality of pairs of a first electrode and a second electrode, and

a plurality of discharge cells with walls, each discharge cell having one of the
5 pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by including a set-up period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge cells, and

a write period of applying a write pulse to selected discharge cells of the plurality
10 of discharge cells based on image data input,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse via the first electrodes, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising, and

15 wherein the driving circuit is operable to apply, during the write period, the write pulse to the selected discharge cells via the first electrodes.

73. (Previously Presented) The plasma display apparatus according to Claim 72, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $5.25 \text{ V}/\mu\text{s}$ and no greater than $6 \text{ V}/\mu\text{s}$.

74. (Previously Presented) The plasma display apparatus according to Claim 72, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

75. (Previously Presented) The plasma display apparatus according to Claim 72, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

76. (Previously Presented) The plasma display apparatus according to Claim 72, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

77. (Previously Presented) The plasma display apparatus according to Claim 72,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

78. (Previously Presented) The plasma display apparatus according to Claim 72,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

79. (Previously Presented) The plasma display apparatus according to Claim 72,
wherein the driving circuit is operable to apply, during the set-up period, a portion of the set-up pulse having a characteristic of rising at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ from 0 volts to a peak voltage and falling from the peak voltage to 0 volts at a rate greater than the average voltage change rate at a time period of the rising.

80. (Previously Presented) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode, the plasma display panel driving method including the following step to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6

V/ μ s from 0 volts to a peak voltage and that falls from the peak voltage to 0 volts at a rate greater than the average voltage change rate at a time period of the rising.

81. (Previously Presented) In a plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode, the plasma display panel driving method applying a voltage waveform to perform an image display, the improvement comprising:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s from a rising set up low point to a maximum value and falls at a rate greater than the average voltage change rate at a time period of the rising to a falling setup low point.

82. (Previously Presented) A plasma display panel driving method for a plasma display panel having a plurality of pairs of display electrodes, a plurality of data electrodes arranged to intersect the display electrodes, and a plurality of discharge cells each formed in a space between the display electrodes and the data electrodes, the plasma display panel driving
5 method repeating the following steps to perform image display:

a set-up step for applying a set-up pulse to one of each pair of display electrodes;

a write step for applying a write pulse to selected data electrodes of the plurality of data electrodes to write an image; and

a discharge sustain step for applying at least one sustain pulse across the pairs of
10 display electrodes after the write step to perform a sustain discharge in discharge cells related to the written image,

wherein the set-up pulse applied during the set-up step has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$, and

15 wherein in the write step, a scan pulse is applied to one of each pair of display electrodes, the scan pulse comprising a drop in voltage from a predetermined reference potential.

83. (Previously Presented) The plasma display panel driving method according to Claim 82, wherein the set-up pulse applied in the setup step rises at an average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

84. (Previously Presented) The plasma display panel driving method according to Claim 82, wherein the set-up pulse applied in the set-up step rises at an average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

85. (Previously Presented) The plasma display panel driving method according to Claim 82, wherein the set-up pulse applied in the set-up step rises at an average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

86. (Previously Presented) The plasma display panel driving method according to Claim 82, wherein the set-up pulse applied in the set-up step rises at an average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

87. (Previously Presented) The plasma display panel driving method according to Claim 82, wherein the set-up pulse applied in the set-up step -rises at an average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

88. (Currently Amended) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of display electrodes, a plurality of data electrodes arranged to intersect the display electrodes, and a plurality of discharge cells each formed in a space between ~~between~~ the display electrodes and the data
5 electrodes; and

a driving circuit operable to drive the plasma display panel by repeating a set-up period of applying a set-up ~~pulse~~ pulse to one of each pair of display electrodes, a write period of applying a write pulse to selected data electrodes of the plurality of data electrodes to write an image, and a discharge sustain period for applying at least one sustain pulse across the pair of
10 display electrodes after the write step to perform a sustain discharge in discharge cells related to the written image,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s, and to apply, during the write period, a scan pulse to one of each pair of display electrodes,
15 the scan pulse comprising a drop in voltage from a predetermined reference potential.

89. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the scan pulse is a negative logic pulse.

90. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than 4.2 V/ μ s and no greater than 5.25 V/ μ s.

91. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

92. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

93. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

94. (Previously Presented) The plasma display apparatus according to Claim 88, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

95. (Previously Presented) The plasma display apparatus according to Claim 89, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

96. (Previously Presented) The plasma display apparatus according to Claim 89, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

97. (Previously Presented) The plasma display apparatus according to Claim 89, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

98. (Previously Presented) The plasma display apparatus according to Claim 89, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

99. (Previously Presented) The plasma display apparatus according to Claim 89, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at an average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

100. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having, a first substrate, a second substrate facing the first substrate, a pair of a first electrode and a second electrode arranged on the first substrate, and a third electrode arranged on the second substrate, the plasma display panel configured to cause a surface discharge between each pair of the first electrode and the second electrode, and the plasma display panel driving method including the following step to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$, and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

101. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a first substrate, a second substrate facing the first substrate, a pair of a first electrode and a second electrode arranged on the first substrate, and a third electrode arranged on the second substrate, the plasma display panel configured to cause a surface discharge between each pair of the first electrode and the second electrode, and the plasma display panel driving method including the following steps to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells; and

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells based on image data input,

wherein in the set-up step, the set-up pulse is applied via the first electrodes and has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising,

15 and

wherein in the write step, the write pulse is applied to the selected discharge cells via the first electrodes.

102. (New) A plasma display apparatus comprising:

a plasma display panel including

a first substrate,

a second substrate facing the first substrate,

5 a plurality of pairs of a first electrode and a second electrode arranged on the first substrate,

a plurality of third electrodes arranged on the second substrate, and

a plurality of discharge cells with walls, each discharge cell having one of the pairs of the first electrode and the second electrode, wherein the plasma display panel is configured to cause a surface discharge between each pair of the first electrode and the second electrode; and

10 a driving circuit operable to drive the plasma display panel by including a set-up period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge cells, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

103. (New) A plasma display apparatus comprising:

a plasma display panel including

a first substrate,

a second substrate facing the first substrate,

5 a plurality of pairs of a first electrode and a second electrode arranged on
the first substrate,

a plurality of third electrodes arranged on the second substrate, and

a plurality of discharge cells with walls, each discharge cell having one of

the pairs of the first electrode and the second electrode, wherein the plasma display panel

10 is configured to cause a surface discharge between each pair of the first electrode and the
second electrode; and

a driving circuit operable to drive the plasma display panel by including a set-up
period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge
cells, and

15 a write period of applying a write pulse to selected discharge cells of the plurality
of discharge cells based on image data input,

wherein the driving circuit is operable to apply, during the set-up period, the set-
up pulse via the first electrodes, the set-up pulse having a waveform that rises at an average
voltage change rate of no greater than 6 V/ μ s and that starts to fall at a rate greater than the
20 average voltage change rate at a time period of the rising, and

wherein the driving circuit is operable to apply, during the write period, the write
pulse to the selected discharge cells via the first electrodes.

104. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a first substrate, a second substrate facing the first substrate, a pair of a first electrode and a second electrode arranged on the first substrate, and a third electrode arranged on the second substrate, the plasma display panel configured to cause a surface discharge between each pair of the first electrode and the second electrode, and the plasma display panel driving method including the following step to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s from 0 volts to a peak voltage and that falls from the peak voltage to 0 volts at a rate greater than the average voltage change rate at a time period of the rising.

105. (New) In a plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a first substrate, a second substrate facing the first substrate, a pair of a first electrode and a second electrode arranged on the first substrate, and a third electrode arranged on the second substrate, the plasma display panel configured to cause a surface discharge between each pair of the first electrode and the second electrode, and the plasma display panel driving method applying a voltage waveform to perform an image display, the improvement comprising:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6

V/ μ s from a rising set up low point to a maximum value and falls at a rate greater than the average voltage change rate at a time period of the rising to a falling setup low point.

106. (New) A plasma display panel driving method for a plasma display panel having a first substrate, a second substrate facing the first substrate, a plurality of pairs of a first display electrode and a second display electrode arranged on the first substrate, a plurality of data electrodes arranged on the second substrate to intersect the plurality of pairs of the first display electrode and the second display electrode, and a plurality of discharge cells each formed in a space between the plurality of pairs of the first display electrode and the second display electrode and the plurality of data electrodes, the plasma display panel configured to cause a surface discharge between each pair of the first display electrode and the second display electrode, and the plasma display panel driving method repeating the following steps to perform image display:

10 a set-up step for applying a set-up pulse to one of each pair of the first display electrode and the second display electrode;

a write step for applying a write pulse to selected data electrodes of the plurality of data electrodes to write an image; and

15 a discharge sustain step for applying at least one sustain pulse across the pairs of the first display electrode and the second display electrode after the write step to perform a sustain discharge in discharge cells related to the written image,

wherein the set-up pulse applied during the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s, and

wherein in the write step, a scan pulse is applied to one of each pair of the first
20 display electrode and the second display electrode, the scan pulse comprising a drop in voltage
from a predetermined reference potential.

107. (New) A plasma display apparatus comprising:

a plasma display panel that includes

a first substrate,

a second substrate facing the first substrate,

5 a plurality of pairs of a first display electrode and a second display
electrode arranged on the first substrate,

a plurality of data electrodes arranged on the second substrate to intersect
the display electrodes, and

a plurality of discharge cells each formed in a space between the plurality
10 of pairs of the first display electrode and the second display electrode and the plurality of
data electrodes, wherein the plasma display panel is configured to cause a surface
discharge between each pair of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by repeating a set-up
period of applying a set-up pulse to one of each pair of the first display electrode and the second
15 display electrode, a write period of applying a write pulse to selected data electrodes of the
plurality of data electrodes to write an image, and a discharge sustain period for applying at least
one sustain pulse across the pair of the first display electrode and the second display electrode
after the write step to perform a sustain discharge in discharge cells related to the written image,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s, and to apply, during the write period, a scan pulse to one of each pair of the first display electrode and the second display electrode, the scan pulse comprising a drop in voltage from a predetermined reference potential.

108. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode covered by a dielectric layer, the plasma display panel driving method including the following step to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s, and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

109. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode covered by a dielectric layer, the plasma display panel driving method including the following steps to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells; and

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells based on image data input,

wherein in the set-up step, the set-up pulse is applied via the first electrodes and
10 has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that
starts to fall at a rate greater than the average voltage change rate at a time period of the rising,
and

wherein in the write step, the write pulse is applied to the selected discharge cells
via the first electrodes.

110. (New) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of a first electrode and a
second electrode covered by a dielectric layer, and

a plurality of discharge cells with walls, each discharge cell having one of the
5 pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by including a set-up
period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge
cells, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse
having a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that
10 starts to fall at a rate greater than the average voltage change rate at a time period of the rising.

111. (New) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of a first electrode and a
second electrode covered by a dielectric layer, and

a plurality of discharge cells with walls, each discharge cell having one of the
5 pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by including a set-up period of applying a set-up pulse to the discharge cells to increase wall charges in the discharge cells, and

10 a write period of applying a write pulse to selected discharge cells of the plurality of discharge cells based on image data input,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse via the first electrodes, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time period of the rising, and

15 wherein the driving circuit is operable to apply, during the write period, the write pulse to the selected discharge cells via the first electrodes.

112. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode covered by a dielectric layer, the plasma display panel driving method including the following step to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$ from 0 volts to a peak voltage and that falls from the peak voltage to 0 volts at a rate greater than the average voltage change rate at a time period of the rising.

113. (New) In a plasma display panel driving method for a plasma display panel in which a plurality of discharge cells having walls are arranged, each discharge cell having a pair of a first electrode and a second electrode covered by a dielectric layer, the plasma display panel driving method applying a voltage waveform to perform an image display, the improvement comprising:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to increase wall charges in the discharge cells, wherein a portion of the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no greater than 6 V/ μ s from a rising set up low point to a maximum value and falls at a rate greater than the average voltage change rate at a time period of the rising to a falling setup low point.

114. (New) A plasma display panel driving method for a plasma display panel having a plurality of pairs of a first display electrode and a second display electrode covered by a dielectric layer, a plurality of data electrodes arranged to intersect the plurality of pairs of the first display electrode and the second display electrode, and a plurality of discharge cells each formed in a space between the plurality of pairs of the first display electrode and the second display electrode and the data electrodes, the plasma display panel driving method repeating the following steps to perform image display:

a set-up step for applying a set-up pulse to one of each pair of the first display electrode and the second display electrode;

a write step for applying a write pulse to selected data electrodes of the plurality of data electrodes to write an image; and

a discharge sustain step for applying at least one sustain pulse across the pairs of the first display electrode and the second display electrode after the write step to perform a sustain discharge in discharge cells related to the written image,

15 wherein the set-up pulse applied during the set-up step has a waveform that rises at an average voltage change rate of no greater than $6 \text{ V}/\mu\text{s}$, and

 wherein in the write step, a scan pulse is applied to one of each pair of the first display electrode and the second display electrode, the scan pulse comprising a drop in voltage from a predetermined reference potential.

115. (New) A plasma display apparatus comprising:

 a plasma display panel that includes a plurality of pairs of a first display electrode and a second display electrode, a plurality of data electrodes arranged to intersect the pairs of the first display electrode and the second display electrode, and a plurality of discharge cells each
5 formed in a space between the pairs of the first display electrode and the second display electrode and the data electrodes; and

 a driving circuit operable to drive the plasma display panel by repeating a set-up period of applying a set-up pulse to one of each pair of the first display electrode and the second display electrode, a write period of applying a write pulse to selected data electrodes of the
10 plurality of data electrodes to write an image, and a discharge sustain period for applying at least one sustain pulse across the pair of the first display electrode and the second display electrode after the write step to perform a sustain discharge in discharge cells related to the written image,

 wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse having a waveform that rises at an average voltage change rate of no greater than 6

- 15 V/ μ s, and to apply, during the write period, a scan pulse to one of each pair of the first display electrode and the second display electrode, the scan pulse comprising a drop in voltage from a predetermined reference potential.